

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 251390US2		SERIAL NO. 10/814,345	
<div style="display: flex; align-items: center; justify-content: center;"> <div style="border: 1px solid black; border-radius: 50%; padding: 10px; text-align: center; margin: 0 10px;"> OIPE JCI26 AUG 18 2004 PATENT & TRADEMARK OFFICE </div> <div> LIST OF REFERENCES CITED BY APPLICANT </div> </div>				APPLICANT Tomoaki SHINO			
				FILING DATE April 1, 2004		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
SWL	AW	T. OHSAWA, et al., International Solid-State Circuits Conference, Digest of Technical Papers, page 153, "MEMORY DESIGN USING ONE-TRANSISTOR GAIN CELL ON SOI", 2002					
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner CRA NC					Date Considered 8/2005		
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							